



[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

translate HDL to C

1995

- 2004

Ad
Sc
Sc

"to" is a very common word and was not included in your search. [\[details\]](#)

Scholar All articles - **Recent articles** Results 1 - 10 of about 6,140 for **translate HDL to C**. (0.12 sec)

All Results

[M Bodzioch](#)

[E Orsó](#)

[J Klucken](#)

[T Langmann](#)

[A Böttcher](#)

[The gene encoding ATP-binding cassette transporter 1 is mutated in Tangier disease - all 5 versions »](#)

... W Drobnik, S Barlage, C Büchler, M Porsch- ... - Nature Genetics, 1999 - nature.com
... The **translation** product is predicted to be non-functional because it ... addition, family members identified as heterozygotes showed decreased **HDL-C** plasma levels ...

Cited by 767 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Association of Cholesteryl Ester Transfer Protein-Taq IB Polymorphism With Variations in Lipoprotein ... - all 5 versions »](#)

... JD Otvos, D Osgood, A Martinez, C Lahoz, O Coltell ... - Arteriosclerosis, Thrombosis, and Vascular Biology, 2000 - Am Heart Assoc
... **HDL-C** variability in men and women and apoA-I levels and LDL size in men and accounts for 1% of the variability in **HDL-C**. These associations **translate** into a ...

Cited by 189 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Architecture validation for processors - all 7 versions »](#)

RC Ho, CH Yang, MA Horowitz, DL Dill - ACM SIGARCH Computer Architecture News, 1995 - portal.acm.org

... description, usually in the form of an **HDL** such as ... all models directly from Verilog using a **translator** to the ... With this subset, **translation** is mostly a one-to ...

Cited by 119 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[UML-Based Analysis of Embedded Systems Using a Mapping to VHDL - all 12 versions »](#)

WE McUumber, BHC Cheng - The 4th IEEE International Symposium on High-Assurance ..., 1999 - doi.ieeecomputersociety.org

... for the Fur- naceRelay component of the model generated by our UML to **VHDL translator**. ...

Discussion Our **translation** to and simulation in **VHDL** has been ...

Cited by 32 - [Related Articles](#) - [Web Search](#)

[SpecCharts: a VHDL front-end for embedded systems - all 3 versions »](#)

F Vahid, S Narayan, DD Gajski - Computer-Aided Design of Integrated Circuits and Systems, ..., 1995 - ieeeexplore.ieee.org

... into a **VHDL** design environment using automatic **translation** to **VHDL** ... a few hours, and because we can easily **translate** Speccharts to ... or we could apply a **VHDL** to **C** ...

Cited by 41 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Effects of Raloxifene on Serum Lipids and Coagulation Factors in Healthy Postmenopausal Women - all 7 versions »](#)

BW Walsh, LH Kuller, RA Wild, S Paul, M Farmer, JB ... - JAMA, 1998 - Am Med Assoc
... in fibrinogen induced by raloxifene in this study could therefore **translate** into an ... between estrogen and raloxifene is in their effect on **HDL-C**, **HDL 2 -C**, and ...

Cited by 530 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Hardware synthesis from C/C++ - all 2 versions »

A Ghosh, J Kunkel, S Liao - Proceedings of the conference on Design, automation and test ... , 1999 - portal.acm.org

... flow, designer productivity can be significantly improved because we can eliminate **translation** into an **HDL** by synthesizing directly from **C/C++** specifications. ...

Cited by 39 - [Related Articles](#) - [Web Search](#)

A C-based RTL design verification methodology for complex microprocessor - all 10 versions »

JS Yim, YH Hwang, CJ Park, H Choi, WS Yang, HS Oh, ... - Proceedings of the 34th annual conference on Design ... , 1997 - portal.acm.org

... But there is no need to **translate** Verilog model into intermediate **C** code. ... using **C** is more difficult than the well- formalized **VHDL** or Verilog in many ... **translation** ...

Cited by 26 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

A design framework for asynchronous/synchronous circuits based on CHP to HDL translation - all 7 versions »

M Renaudin, P Vivet, F Robin - Proc. International Symposium on Advanced Research in ... , 1999 - doi.ieeecs.org

... The CHP2VHDL **translator** is designed to bring maximum ... deal with the CHP to **VHDL translation** itself, focusing ... The CHP2VHDL tool is written in **C** (about 3000 lines ...

Cited by 21 - [Related Articles](#) - [Web Search](#)

(ps) HDL-based integration of formal methods and CAD tools in the PREVAIL environment - all 11 versions »

D Borriane, H Bouamama, D Deharbe, C Le Faou, A ... - FMCAD, 1996 - monolith.consiste.dimap.ufrn.br

... is very sensitive to the way the **VHDL** code is ... to the simplification process included in the **translator**. Some **translation** schemes simplify the generated code in ...

Cited by 11 - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [BL Direct](#)

Google 

Result Page: 1 2 3 4 5 6 7 8 9 10 [Next](#)

translate HDL to C

Search

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2007 Google

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20030061580" and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:28
L2	2	"6868545".pn. and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:24
L3	1934	(hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:30
L4	1820	(hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:31
L5	899	(hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver) and (library or pallet or libraries or selection) and (interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:32
L6	84	717/???.ccls. and (hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver) and (library or pallet or libraries or selection) and (interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:34
L7	0	716/???.ccls. and (hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver) and (library or pallet or libraries or selection) and (interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:33
L8	131	717/???.ccls. and (vhdl or hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver) and (library or pallet or libraries or selection) and (interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:51

EAST Search History

L9	0	716/???.ccls. and (vhdl or hdl or hardware adj (design or definition)) same (compil\$5 or transform\$5) and (control\$4 or driver) and (library or pallet or libraries or selection) and (interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:34
L10	2	"6983456".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:52
L11	13	717/135.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:45
L12	11	717/136.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:46
L13	5	717/137.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:46
L14	5	717/138.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:46
L15	4	717/139.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:46

EAST Search History

L16	125	716/3.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:13
L17	154	716/4.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") and (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:47
L18	34	I11 I12 I13 I14 I15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 15:48
L19	35	716/3.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") same (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:13
L20	43	716/4.ccls. and ("c" or "c++" or modern or "high level" or java or "object oriented") same (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:48
L21	72	I19 I20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:46
L22	3	"20030154464"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:46

EAST Search History

L23	19	Bollano.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:54
L24	2	("2003/0154465").URPN.	USPAT	OR	OFF	2007/09/10 16:48
L25	2	l24 and ("c" or "c++" or modern or "high level" or java or "object oriented") same (hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:49
L26	2	l24 and ("c" or "c++" or modern or "high level" or java or "object oriented") same (vhdl or hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:49
L27	1	"20030154465" and ("c" or "c++" or modern or "high level" or java or "object oriented") same (vhdl or hdl or (hardware adj (design or definition))) same (compil\$5 or transform\$5) and (control\$4 or driver or library or pallet or interface or api or wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 16:55
S1	2457632	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse) and (verilog and "c++" or "c" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:58
S2	618	717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:57
S3	189	717/137.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:57
S4	180	717/138.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:58

EAST Search History

S5	286	717/139.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:58
S6	1495	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse) and (verilog and "c++" or "c" or "cpp" or "c plus plus") and 717/13?.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 17:58
S7	0	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse) and (verilog and "c++" or "c" or "cpp" or "c plus plus") and 717/13?.ccls. and "verilog to" near2 ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:00
S8	4	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse) and (verilog and "c++" or "c" or "cpp" or "c plus plus") and 717/13?.ccls. and verilog near3 ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:09
S9	10	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) and (verilog and "c++" or "c" or "cpp" or "c plus plus") and 717/13?.ccls. and (verilog or hdl or rtl or vhdl) near3 ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:20
S10	6	S9 not S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:10
S11	406	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) same (verilog or hdl or rtl or vhdl) same ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:21

EAST Search History

S12	176	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhdl) same ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:24
S13	65	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhdl) near5 ("c++" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:23
S14	1	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhdl) same ("c++" or "cpp" or "c plus plus") and ("vbscript" or "visual basic script")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:25
S15	37	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhdl) same ("c++" or "cpp" or "c plus plus") and (macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:26
S16	136	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhdl) same ("c++" or "cpp" or "c plus plus") and ("task library" or library or driver or header or interface or api)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:27

EAST Search History

S17	64	(compil\$5 or convert\$3 or conversion or translat\$3 or transform\$5 or (cross near3 compil\$5) or reverse or map\$4) near5 (verilog or hdl or rtl or vhd) same ("c++" or "cpp" or "c plus plus") and ("task library" or library or driver or header or interface or api) and (verilog or hdl or rtl or vhd) near5 ("# delay" or "ifdef" or "{" or "}" or (register near3 definition) or assignment or event or switch or "case statement" or concat or concatenation or "#define" or const or constant or (bit near3 access\$3)) and (substitut\$3 or translat\$3 or remov\$3 or convert\$3 or insert\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/09 18:34
S18	6	("6097212" "6188975" "6415420" "6507947").PN. OR ("6606734").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/09 18:53
S19	718	716/3.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:30
S20	46	716/3.ccls. and (verilog or vhd) or hdl) near5 ("c++" or "c" or "cpp" or "c plus plus")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:31
S21	2	(hdl or vhd) or (hardware near2 ((descript\$3 or design) adj language)) or verilog) same (map\$4 or substitut\$3 or transform\$5 or translat\$3 or convert\$3 or conversion) same ("c++" or "cpp" or "c plus plus" or ("high level" or modern or "4th") near2 language)) and ((task or process or thread or function\$5) near2 (dll or library or libraries)) and driver same ("interface header" or (interface near2 (header or prolog)) or wrap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 16:44

EAST Search History

S22	2	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) same (map\$4 or substitut\$3 or transform\$5 or translat\$3 or convert\$3 or conversion) same ("c++" or "cpp" or "c plus plus" or (("high level" or modern or "4th") near3 language)) and ((task or process or thread or function\$5) near2 (dll or library or libraries)) and driver same ("interface header" or (interface near2 (header or prolog)) or wrap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 16:44
S23	134	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and ("# delay" or "accent grave" or "compiler directive" or ifdef)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 16:54
S24	22	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and ("# delay" or "accent grave" or "compiler directive" or ifdef) and wrapper	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 17:01
S25	0	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and ("# delay" or "accent grave" or "compiler directive" or ifdef) and (driver same wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 17:02
S26	75	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and (driver same wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 17:04
S27	7	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and (add\$3 or insert\$3 or modify\$3 or edit\$3 or includ\$3) same (driver same wrapper)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 17:14
S28	188	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and (controller or "control signal" or driver) same wrapper and driver and wrapper	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/06 17:20

EAST Search History

S29	185	(hdl or vhdl or (hardware near2 ((descript\$3 or design) adj language)) or verilog) and (controller or "control signal" or driver) same wrapper and driver and wrapper and (map\$4 or substitut\$3 or transform\$5 or translat\$3 or convert\$3 or conversion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 12:12
S30	2	"20030061580"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 13:20
S31	3	("2003/0061580").URPN.	USPAT	OR	OFF	2007/09/10 12:13
S32	0	"20030061580" and driver and (library or dll) and (interface or api) and (header or prolog\$2 or defin\$3 or macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 13:21
S33	0	"20030061580" and driver and (library or dll) and (interface or api or wrapper) and (header or prolog\$2 or defin\$3 or macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/10 14:21